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SEPTEMBER 2006
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TECHNOLOGY

FOR ENGINEERS, DESIGNERS AND INTEGRATORS

The CEOs

of the
Power Electronics
Industry

A SUPPLEMENT TO POWER ELECTRONICS TECHNOLOGY



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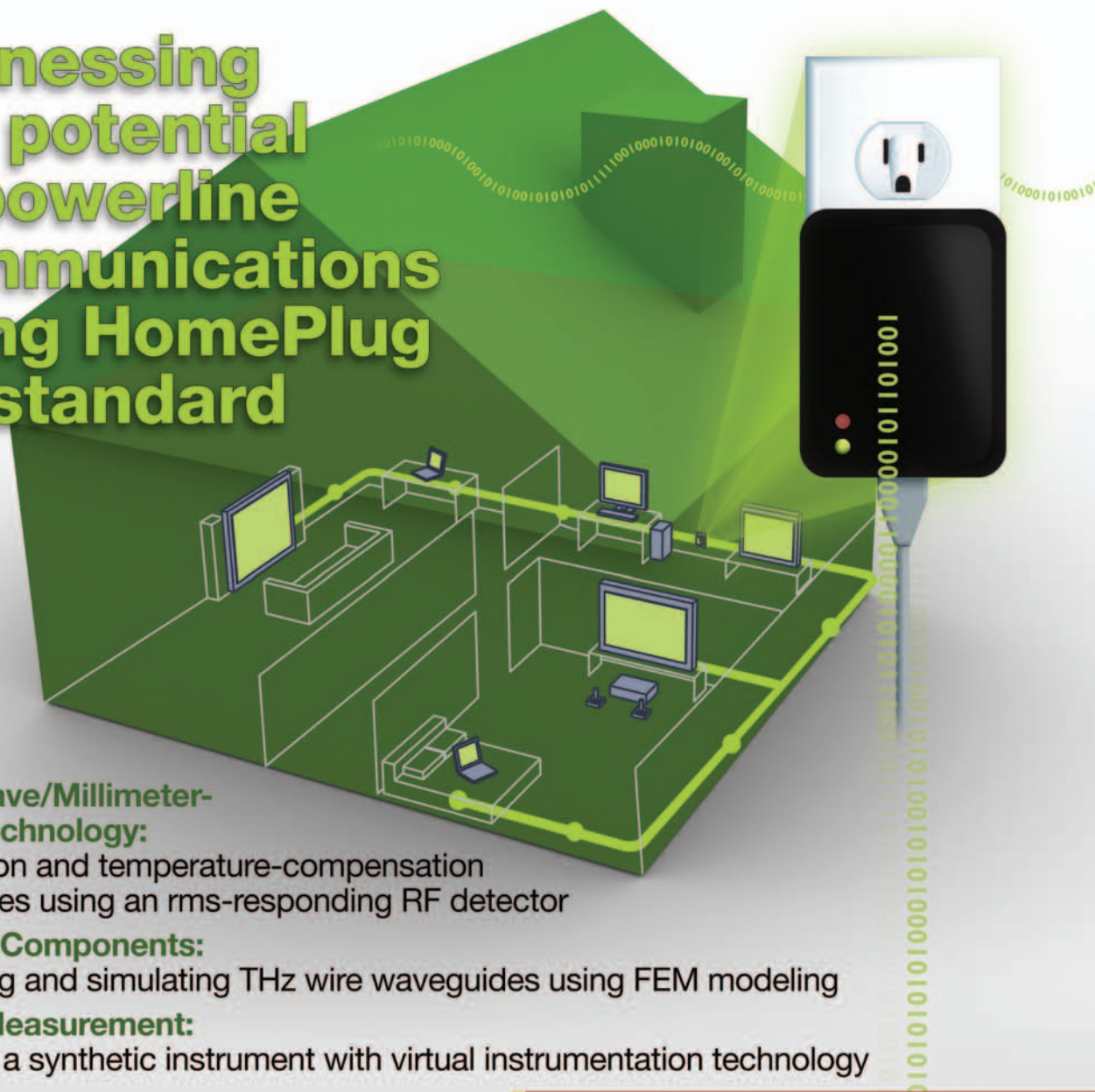
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RF DESIGN

RF AND MICROWAVE TECHNOLOGY FOR DESIGN ENGINEERS

Harnessing the potential of powerline communications using HomePlug AV standard



Microwave/Millimeter-Wave Technology:

Calibration and temperature-compensation techniques using an rms-responding RF detector

Passive Components:

Designing and simulating THz wire waveguides using FEM modeling

Test & Measurement:

Creating a synthetic instrument with virtual instrumentation technology

DESIGN TIP

Measuring high-speed signals with an active differential probe

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Implantable ultralow-power radio chip facilitates in-body communications

Test & Measurement:

MIMO WLAN test methodologies for manufacturing

Active Components:

How to determine an effective damping factor for a third-order PLL

Amplifiers:

Highly efficient amplifier shows the promise of Doherty architecture

DESIGN TIP

Characterizing phase-locked loops with waveform scans

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RF DESIGN

RF AND MICROWAVE TECHNOLOGY FOR DESIGN ENGINEERS



Built-in handset antennas enable FM transceivers in mobile phones

Time & Frequency:

Direct digital synthesis enables digital PLLs

Next-Generation Wireless:

Adopting an RFIC application-programming interface

RF/Microwave Switches & Connectors:

Overcoming the RF challenges of multiband mobile handset design

DESIGN TIP

Alternative to SMT for microwave and millimeter-wave systems

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